DAQ-2500 Series

4/8-CH 12-Bit 1 MS/s Analog Output Multi-Function DAQ Cards

Features

- ■Supports a 32-bit 3.3 V or 5 V PCI bus
- ■12-bit multiplying D/A resolution
- ■Up to 1 MS/s simultaneous-update rate
- ■4-CH multiplying analog outputs (DAQ-2501)
- ■8-CH multiplying analog outputs (DAQ-2502)
- ■Hardware-based arbitrary waveform generation
 ■On-board 8 k-sample D/A FIFO (DAQ-2501)
- ■On-board 16 k-sample D/A FIFO (DAQ-2502)
- ■Programmable bipolar or unipolar analog output ranges on per channel basis
- ■Programmable internal or external reference sources on per channel basis
- ■8-CH 400 kS/s 14-bit single-ended analog inputs (DAQ-2501)
- ■4-CH 400 kS/s 14-bit single-ended analog inputs (DAQ-2502)
- ■On-board 2k-sample A/D FIFO
- ■Bipolar or unipolar analog input ranges
 ■Scatter-gather DMA for both analog inputs and outputs
- ■24-CH TTL digital input/output
- ■2-CH 16-bit general purpose timer/counters
- ■Analog & digital triggering
- ■Fully auto-calibration
- Multiple cards synchronization through SSI (System Synchronization Interface) bus
- **■**Operating Systems
- Windows 2000/NT/XP/98
- Red Hat Linux
- Windows CE (call for availability)
- **■**Recommended Software
- □ IVB/VC++/BCB/Delphi
- DAQBench

■Driver Support

- •D2K-DASK:
- Windows 2000/NT/XP/98 driver
- •D2K-DASK/X: Red Hat Linux driver
- •D2K-LVIEW: LabVIEW driver
- •D2K-MTLB: MATLAB driver
- D2K-OCX: 32-bit ActiveX controls



Introduction

ADLINIK DAQ-2501 and DAQ-2502 are high-speed and high-performance analog output multifunction DAQ cards. The devices are able to update up to 8-CH, 12-bit analog outputs simultaneously at sustaining 1 MS/s. The reference sources and the output polarities are programmable on per channel basis, combining with the multiplying DAC architecture, ADLINK DAQ-2500 series DAQ cards can generate complex modulated analog signals. The hardware-based arbitrary waveform generation frees the CPU intervention even when all analog outputs are updating at full speed, and the lengths of waveforms are only limited by the system memory.

The DAQ-2500 series integrate up to 8-CH, 400 kS/s, 14-bit single-ended analog inputs with programmable polarity, 24-CH programmable digital I/O lines, and 2-CH 16-bit general-purpose timer/counters.

Like all the other members in DAQ-2000 family, the DAQ-2500 series are able to perform the analog input and output functions at full speed simultaneously and multiple cards can be synchronized through the SSI (system synchronization interface) bus. The auto-calibration functions adjust the gain and offset to within specified accuracies such that you do not have to adjust trimpots to calibrate the boards.

Termination Boards

■ DIN-68S/1M

Termination Board with a 68-pin SCSI-II Connector and DIN-Rail Mounting (Including One 1-meter ACL-10568 Cable)



SSI bus cable for multiple cards synchronization

Ordering Information

DAQ-2501

4-CH 12-Bit 1 MS/s Analog Output Multi-Function DAQ Card

DAQ-2502

8-CH 12-Bit 1 MS/s Analog Output Multi-Function DAQ Card



Termination board DIN-68S/1M

Pin Assignment

Connector CN1 Pin Assignment

Connector C	141	- 111	Assignment
AO_0	1	35	AGND
AO_1	2	36	AGND
			AGND
AO_3	4	38	AGND
AOEXTREF_A/AI_0	5	39	AGND
Al_1	6	40	AGND
EXTTRIG/AI_2	7	41	AGND
AOEXTREF_B/AI_3			
AO_4/AI_4	9	43	AGND
AO_5/AI_5	10	44	AGND
AO_6/AI_6	11	45	AGND
AO_7/AI_7	12	46	AGND
AO_TRIG_OUT_A	13	47	EXTWFTRG_A
AO_TRIG_OUT_B	14	48	EXTWFTRG_B
GPTC1_SRC	15	49	VCC
GPTC0_SRC	16	50	DGND
			GPTC1_GATE
			GPTC1_OUT
GPTC0_UPDOWN	19	53	GPTC1_UPDOWN
RESERVED	20	54	DGND
		_	AFI0
PB7	22	56	PB6
		_	PB4
PB3	24	58	PB2
PB1	25	59	PB0
PC7	26	60	PC6
		_	PC4
		_	DGND
		_	PC2
			PC0
			PA6
PA5			PA4

- Pin 9~12 are AI<4..7> for DAQ-2501; AO<4 7> for DAO-2502
- The external references inputs and the external analog trigger share the analog input pins 5, 7, and 8

PA3 33 67 PA2

PA1 34 68 PA0

Quick Selection Guide

Model		An	alog Output		Analog Input			t	DIO	Timer/Counter
number	No. of channels	Resolution	Update rate	Output range	No. of channels	Resolution	Sampling rate	Input range	No. of channels	No. of channels
DAQ-2501	4	12 bits	1 MS/s	±10 V, 0 to 10 V	8	14 bits	400 kS/s	±10 V or 0 to 10 V	24-CH 8255 PIO	2-CH, 16-bit
DAQ-2502	8	12 bits	1 MS/s	±10 V, 0 to 10 V	4	14 bits	400 kS/s	±10 V or 0 to 10 V	24-CH 8255 PIO	2-CH, 16-bit

Specifications

Specifications					
Model Number	DAQ-2501	DAQ-2502			
Analog Output					
Number of channels	4 voltage outputs	8 voltage outputs			
Resolution	12 b				
Output ranges	0-10 V, ±10 V, 0-AOEXTREF, ±AOEXTREF				
Maximum update rate	1 MS/s				
Slew rate	20 V/μs				
Setting time	3 μs to ±0.5 LS				
Offset error	±2 r	· · · · · · · · · · · · · · · · · · ·			
Gain error	±0.02% of m				
Driving capacity	±5 n				
Stability	Any passive load				
Trigger sources	Software, external digital				
Trigger modes	Post-trigger, delay-trigger, and repeated trigger				
FIFO buffer size	8 k samples 16 k samples				
Data transfers	Programmed I/O, scatter-gather DMA				
Analog Input	Programmed i/O, scaller-gamer DiviA				
Resolution	14 bits, no mi	ssing codes			
Number of channels	8 single-ended	4 single-ended			
Maximum sampling rate	400 k	<u>_</u>			
Gain	1				
Bipolar input ranges	±10				
	0-10				
Unipolar input ranges					
Offset error	±1 n				
Gain error	±0.01% (
Input coupling	Power on: Continuous ±30 V. I				
Overvoltage protection					
Input impedance	1 GΩ/	•			
Trigger sources	Software, external digital				
Trigger modes	Post-trigger, delay-trigge				
FIFO buffer size	2 k san				
Data transfers	Polling, scatter	-gatner DMA			
Digital I/O	04.011.0055				
Number of channels	24-CH 8255 programmable input/output				
Compatibility	5 V/TTL				
Data transfers Timer/Counter	Programm	пеа і/О			
Number of channels					
Number of channels	2				
D 1.0	40.1				
Resolution	16 b	its			
Compatibility	5 V/T	its TL			
Compatibility Base clock available		its TL			
Compatibility Base clock available Auto Calibration	5 V/T 40 MHz, external cl	its TL ock up to 10 MHz			
Compatibility Base clock available Auto Calibration On-board reference	5 V/T 40 MHz, external cl +5	its TL ock up to 10 MHz V			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift	5 V/T 40 MHz, external cl +5 ±2 ppr	its "TL ock up to 10 MHz V n/°C			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability	5 V/T 40 MHz, external cl +5	its "TL ock up to 10 MHz V n/°C			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability General Specifications	5 V/T 40 MHz, external cl +5 ±2 ppr ±6 ppm/1	vits TL ock up to 10 MHz V m/°C 000 Hrs			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability General Specifications Dimensions	5 V/T 40 MHz, external cl +5 ±2 ppr ±6 ppm/1	vits TL ock up to 10 MHz V m/°C 000 Hrs including connectors)			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability General Specifications Dimensions Connector	5 V/T 40 MHz, external cl +5 ±2 ppr ±6 ppm/1 175 mm x 107 mm (not 68-pin VHD	v v v v v v ocum			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability General Specifications Dimensions Connector Operating temperature	5 V/T 40 MHz, external cl +5 ±2 ppr ±6 ppm/1 175 mm x 107 mm (not 68-pin VHD 0 to 55	v v v v v v v ocup to 10 MHz v v v ocup to 10 MHz v v v ocup to 10 MHz v ocup to 10			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability General Specifications Dimensions Connector Operating temperature Storage temperature	5 V/T 40 MHz, external cl +5 ±2 ppr ±6 ppm/1 175 mm x 107 mm (not 68-pin VHD 0 to 55 -20 to 8	v V m/°C 000 Hrs including connectors) CI female 5 °C 30 °C			
Compatibility Base clock available Auto Calibration On-board reference Temperature drift Stability General Specifications Dimensions Connector Operating temperature	5 V/T 40 MHz, external cl +5 ±2 ppr ±6 ppm/1 175 mm x 107 mm (not 68-pin VHD 0 to 55	v V m/°C 000 Hrs including connectors) CI female 5 °C 30 °C			