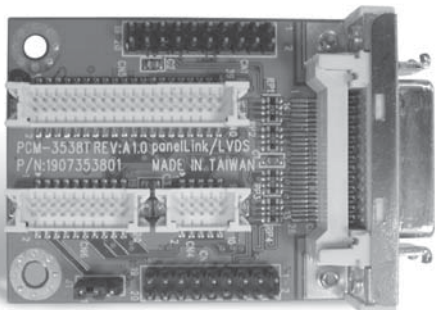


PCM-3538T

Transmitter Modules

Computing Platform Service Partner

TTL to LVDS Transmitter Module



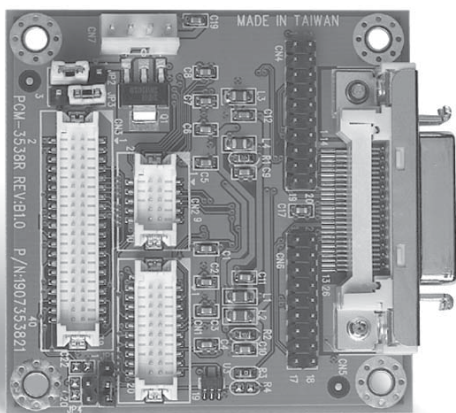
Features

- Using Transmitter Device Module **THC63LVDM83A** Chipset, Transmitter Converts 28-bit of CMOS/TTL Data Into LVDS Data Stream
- Scalable Bandwidth: VGA (640 x 480) to SXGA (1280 x 1024)
- Supports LVDS Channel (3 Data Pairs & 1 Clock Pair) for 18/24-bit LVDS Panel or Up To Two Channels for 18/24/36/48-bit LVDS Panel
- Cable Distance Supports 5M or Above (Depends On Cable Quality)
- Wide Frequency Range:
 1. One Channel (20 ~ 85 MHz)
 2. Two Channels (20 ~ 170 MHz)
- 28:4 Data Channels Compression at up to 245 Mbps Throughput
- Input Port Using TTL Signal With DF13 Connector (2x20, 2x10, 2x5)
- Output LVDS Differential Signal For LVDS Panel

PCM-3538R

Transmitter Modules

LVDS to TTL Receiver Module



Features

- Using **THC63LVDF84A** Chipset (Receiver Module)
- Scalable Bandwidth: VGA (640 x 480) to SXGA (1280 x 1024)
- Supports LVDS Channel (3 Data Pairs & 1 Clock Pair) For 18/24-bit LVDS Panel or up to Two Channels For 18/24/36/48-bit LVDS Panel
- Cable Distance Supports: 5M or Above (Depends On Cable Quality)
- Wide Frequency Range:
 1. One Channel (20 ~ 85 MHz)
 2. Two Channels (20 ~170 MHz)
- 28:4 Data Channel Compression at Up to 245 Mbps Throughput
- Input LVDS Differential Signal From **THC 63LCDM83A** Chipset (PCM-3538T, Transmitter Module)